

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants	:	Taylor, et al.	Art Unit: 2186
App. No.	:	10/732,998	
Filed	:	December 11, 2003	
For	:	CACHE MEMORY ARCHITECTURE AND ASSOCIATED MICROPROCESSOR DESIGN	
Examiner	:	Hetul Patel	

AMENDMENT**Mail Stop RCE**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

In response to the Final Office Action mailed on February 16, 2006, please reconsider the above-referenced application in view of the enclosed Request for Continued Examination and following amendments and remarks.